

PARMA-DITAM Workshop PROGRAM

PARMA 2026: 17th Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures

DITAM 2026: 15th Workshop on Design Tools and Architectures for Multi-Core Embedded Computing Platforms

Location: **S4A** ICE Kraków
Tuesday, 27 January 2026

09:00 – 10:00 *HiPEAC Keynote*

10:00 – 10:15 **PARMA-DITAM Opening session**

Chair:

10:15 – 11:00 **Invited Talk**

Chair:

Paul Carpenter (Barcelona Supercomputing center)

“Distributed task execution: Opportunities, challenges and lessons learnt from OmpSs-2@Cluster”

11:00 – 11:30 *Coffee Break*

11:30 – 13:00 **Session A: Compilers and Cloud HPC Tools**

Chair:

11:30 - 11:50 **Anusha Chatopadhyay (Heidelberg University)**
“SLURM-managed HyperParameter Optimization”

11:50 – 12:10 **Umer Arshad (University of L'Aquila)**
"High Performance Visualization with VisIVO across Cloud and HPC"

12:10 – 12:30 **Giovanni Agosta (Politecnico di Milano)**
"Inter-procedural Strength Reduction for Embedded Systems"

12:30 – 12:50 **Marco De Pascale (Leibniz Supercomputing Centre)**
"LAMINA: An MLIR-Based Translation Library for Heterogeneous Quantum-Classical Compilation"

13:00 – 14:00 *Lunch Break*

14:00 – 14:45 Invited Talk

Chair:

Ivy Peng (KTH Royal Institute of Technology)

“Accelerator-rich Computing in the Post-Moore Era: Opportunities, Challenges, and a Path Forward”

14:45 – 17:30 Session B: Heterogeneous Deployment

Chair:

14:45 - 15:05 Jakob Sachs (Hamburg University of Technology)

“Accelerating GPGPU Simulation by Strategically Parallelizing the Compute Bottleneck”

15:05 – 15:25 Aggelos Ferikoglou (National Technical University of Athens)

“Linking High-Level Synthesis with FPGA Runtime Orchestration”

15:30 – 16:00 Coffee Break

16:00 – 16:45 Invited Talk

Chair:

Daniele Gregori (E4)

“E4 on RISC-V: from Monte Cimone to European Frontiers”

Session B: Heterogeneous Deployment

Chair:

16:45 - 17:05 Georgios Alexandris (National Technical University of Athens)

“Performance Modeling & Mapping of LLM Inference on Heterogeneous Vectorized CGRAs”

17:05 – 17:15 Closing and Final Wrap-up

Chair:

Paper presentations

We invite authors of accepted papers to prepare presentations with slides. Each paper presentation is scheduled to last at most 15 minutes.

Time slot includes:

- 15 min oral presentation
- 5 min Q&A

Presenters should provide a short (no more than 3 lines) biography/introduction to be handed to the Session chair at the beginning of the Session.