

PARMA-DITAM Workshop CALL FOR PAPERS

PARMA-DITAM 2025

PARMA 2025: 16th Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures

DITAM 2025: 14th Workshop on Design Tools and Architectures for Multi-Core Embedded Computing Platforms

The current trend towards many-core and heterogeneous accelerator-based architectures requires a global rethinking of software and hardware design, which turn out to be more than ever before strongly entangled.

The PARMA-DITAM workshop focuses on many-core architectures, parallel programming models, design space exploration, run-time management techniques, tools and methodologies designed to fully exploit many-core processor architectures (possibly heterogeneous and reconfigurable) and boost their performance, from embedded to high performance computing platforms and cyber-physical systems.

TOPIC AREAS

The workshop will have seven main topics:

- T1: Parallel programming models, languages, and applications for many-core platforms
- T2: Compiler and virtualization techniques for novel computing architectures
- T3: Run-time modeling, monitoring, adaptivity, power and memory management
- T4: Design of heterogeneous and reconfigurable many-core architectures
- T5: Methodologies, design tools, and high-level synthesis for heterogeneous architectures
- T6: Hardware/software co-design and design space exploration
- T7: Case studies, success stories and applications applying T1-T6

ORGANIZATION

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PAPER SUBMISSION INSTRUCTIONS

Accepted papers will be published online in the Dagstuhl OASICs OpenAccess Series in Informatics. Papers should be submitted electronically through the EasyChair website:

<https://easychair.org/conferences/?conf=parmaditam2024>

The review process will be **single-blind**. Electronic paper submission requires a full paper, up to 20 single column pages, excluding title page and references. Papers should be submitted in PDF format. Please use the following template when preparing your manuscript: <https://submission.dagstuhl.de/documentation/authors#oasics>

IMPORTANT DATES (new)

Paper submission deadline:	03 December 2024 23:59 UTC
Notification of acceptance:	22 December 2024
Camera ready version:	08 January 2025
Workshop:	21 January 2025 (co-located with HiPEAC)

parma-ditam-workshop.github.io