## **PARMA-DITAM Workshop CALL FOR PAPERS**

# PARMA-DITAM 2023

## PARMA 2023: 14<sup>th</sup> Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures

### DITAM 2023: 12<sup>th</sup> Workshop on Design Tools and Architectures for Multi-Core Embedded Computing Platforms

The current trend towards many-core and the emerging accelerator-based architecture requires a global rethinking of software and hardware design, which turn out to be more than ever before strongly entangled.

The PARMA-DITAM workshop focuses on many-core architectures, parallel programming models, design space exploration, tools and run-time management techniques to exploit the features and boost the performance of such (possibly heterogeneous, (re-)programmable and/or (re-)configurable) many-core processor architectures from embedded to high performance computing platforms and cyber-physical systems.

# TOPIC AREAS

The workshop will have seven main topics:

- T1: Parallel programming models and languages, compilers and virtualization techniques
- T2: Runtime modelling, monitoring, adaptivity, and management
- T3: Runtime trade-off execution, power management, and memory management
- T4: Heterogeneous and reconfigurable many-core: architectures and design space exploration
- T5: Methodologies, design tools, and high level synthesis for many-core architectures
- T6: Parallel applications for many-core platforms
- T7: Case studies, success stories and applications applying T1-T6

# ORGANIZATION

General Chair: Stefano Cherubin, Edinburgh Napier University, s.cherubin@napier.ac.uk

Program Co-Chair: Henri-Pierre Charles, CEA, henri-pierre.charles@cea.fr

**Program Co-Chair: Giuseppe Massari**, Politecnico di Milano, giuseppe.massari@polimi.it

Publication Chair: João Bispo, Universidade do Porto, jbispo@fe.up.pt

Publicity Co-Chair: Amir H. Ashouri, Huawei, amirhossein.ashouri@gmail.com

Publicity Co-Chair: Raquel Lazcano, University of Sassari, rlazcanolopez@uniss.it

**Web+Submission Chair**: **Daniele Cattaneo**, Politecnico di Milano, daniele.cattaneo@polimi.it **Steering Committee**:

- Giovanni Agosta, Politecnico di Milano, agosta@acm.org
- João M.P. Cardoso, Universidade do Porto, jmpc@acm.org
- William Fornaciari, Politecnico di Milano, william.fornaciari@polimi.it
- Michael Hübner, Brandenburg University of Technology Cottbus-Senftenberg, huebner@b-tu.de
- Cristina Silvano, Politecnico di Milano, cristina.silvano@polimi.it
- Dimitrios Soudris, National Technical University of Athens, dsoudris@microlab.ntua.gr

## PAPER SUBMISSION INSTRUCTIONS

We are currently in the process to secure publication of our proceedings in a venue indexed by Scopus/WoS.

Papers should be submitted electronically through the workshop website. The review process will be **double-blind**.

Electronic paper submission requires a full paper, up to 10 single column pages, excluding title page and references. Papers should be submitted in PDF format. Please use the following template when preparing your manuscript: <u>https://submission.dagstuhl.de/documentation/authors#oasics</u>

IMPORIANT DATES (new)	
Paper submission deadline:	11 November 2022 23:59 UTC
Notification of acceptance:	09 December 2022
Camera ready version:	31 December 2022
Workshop:	17 January 2023 (co-located with HiPEAC)