

# Adaptive Accelerators and Processor Architectures

Location: Room **Bianca A**

Scheduled time: **11:30 - 12:15**

## Speaker: Michael Hübner

Prof. Hübner leads the chair for Computer Engineering at the Brandenburg University of Technology in Cottbus. He is currently director of the Computer Science Institute as well. Michael Hübner works on reconfigurable hardware architecture since over 15 years. He started at University Karlsruhe with this research where he received his PhD degree in 2007. In 2011 he received his habilitation for the domain of reconfigurable computing. Michael Hübner is author and co-author of over 250 conference-, journal- and book publications. He is active in several national and international research projects.

## Abstract

Adaptivity of accelerators and processor architecture help to perform algorithms more efficient as with general purpose architectures. However, the development and operation of such adaptive architectures needs to be supported by tools for application integration and run-time support.

This talk presents a selection of several approaches for such architectures.

**PARMA 2020: 11<sup>th</sup> Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures**

**DITAM 2020: 9<sup>th</sup> Workshop on Design Tools and Architectures for Multi-Core Embedded Computing Platforms**